

FIG. 1

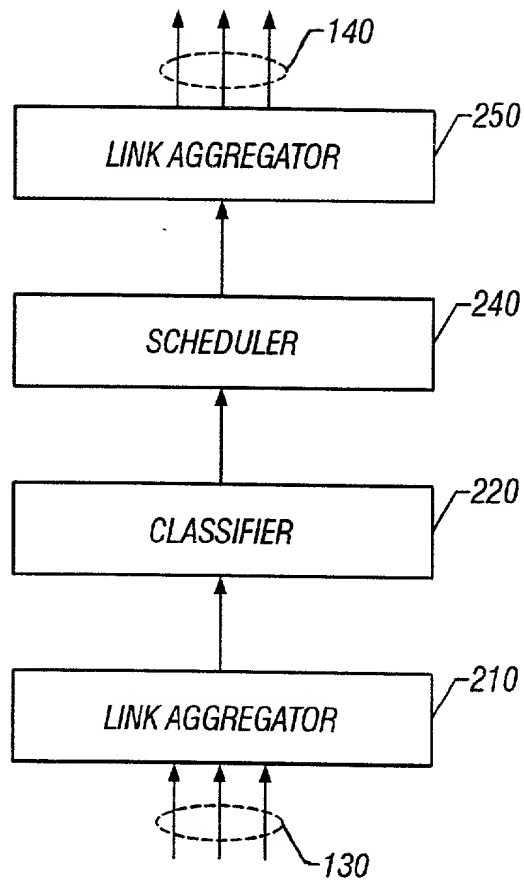


FIG. 2

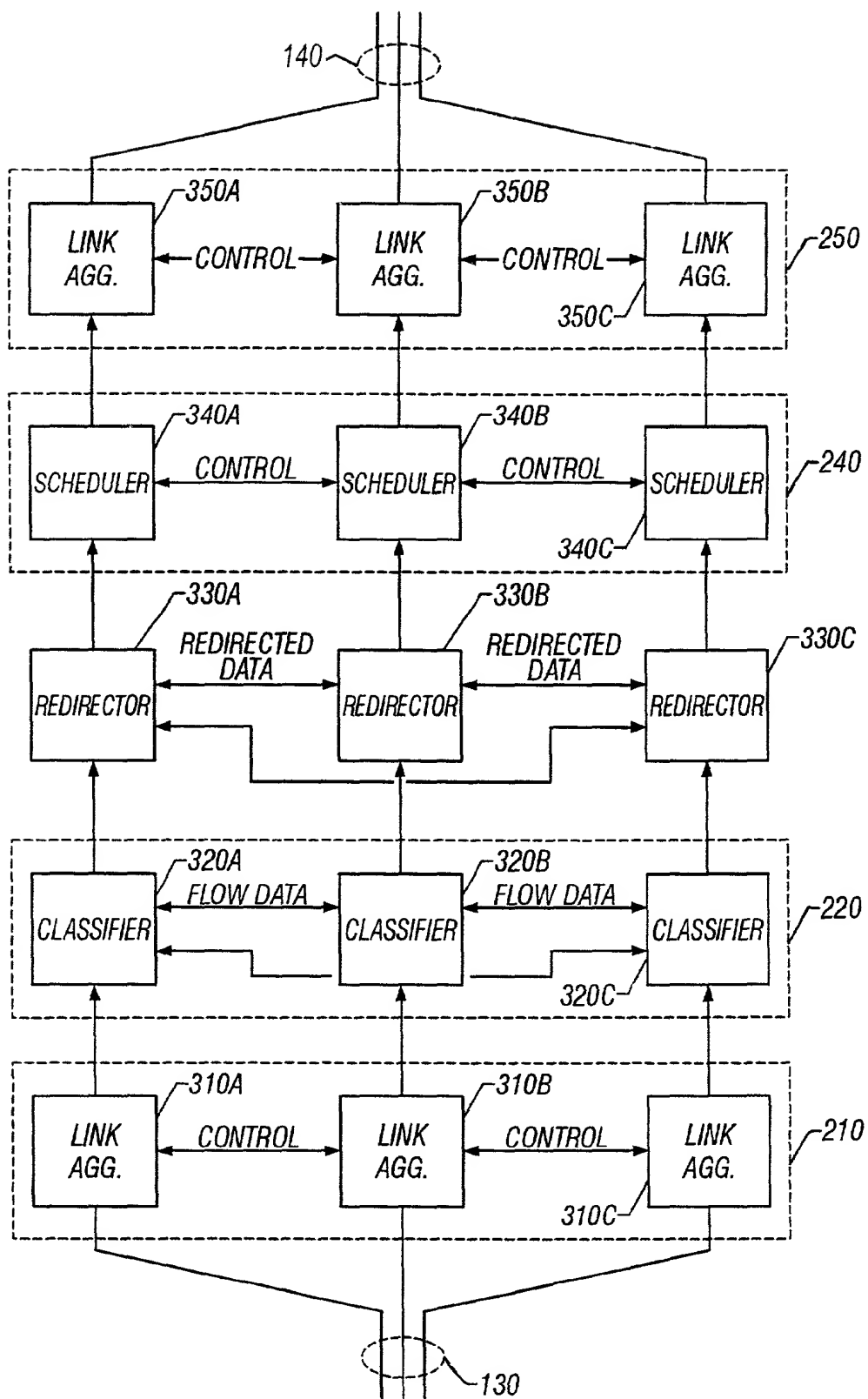


FIG. 3

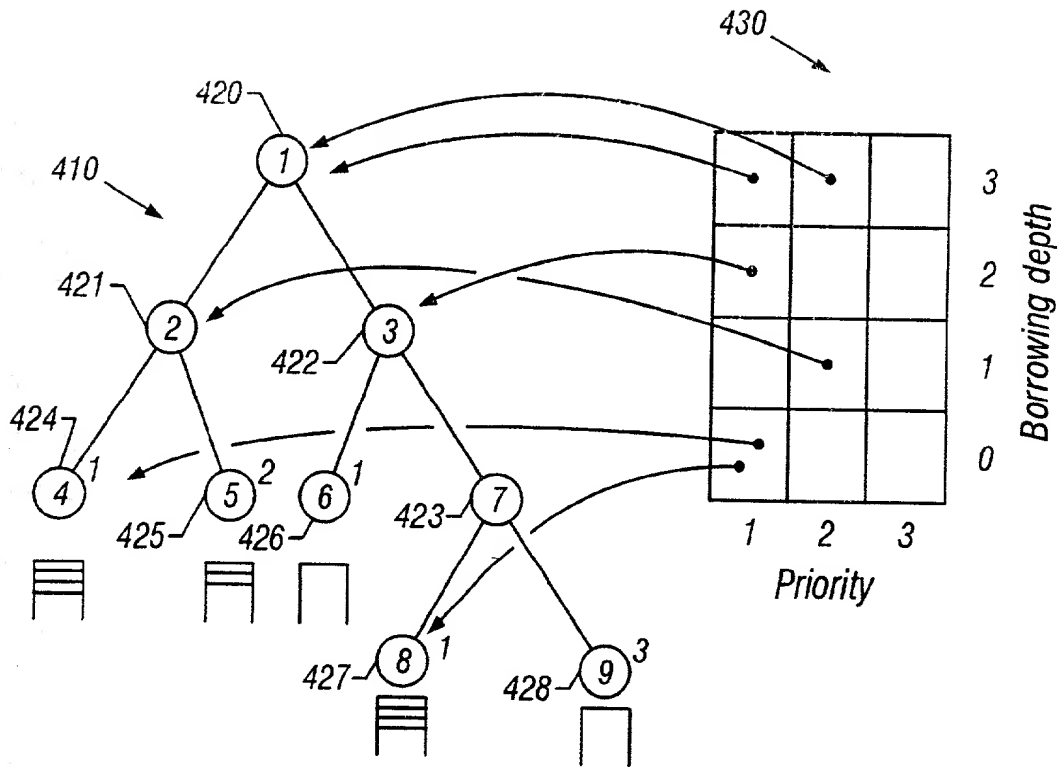


FIG. 4A

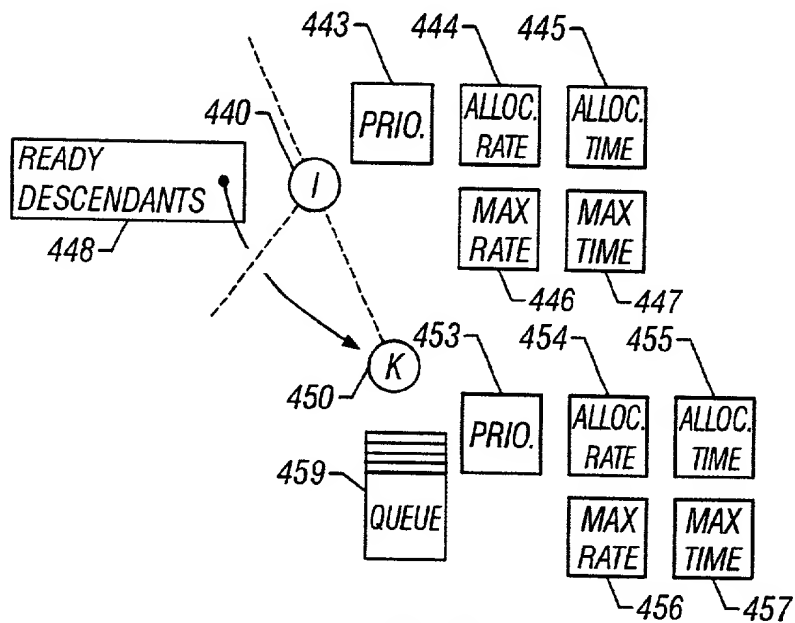


FIG. 4B

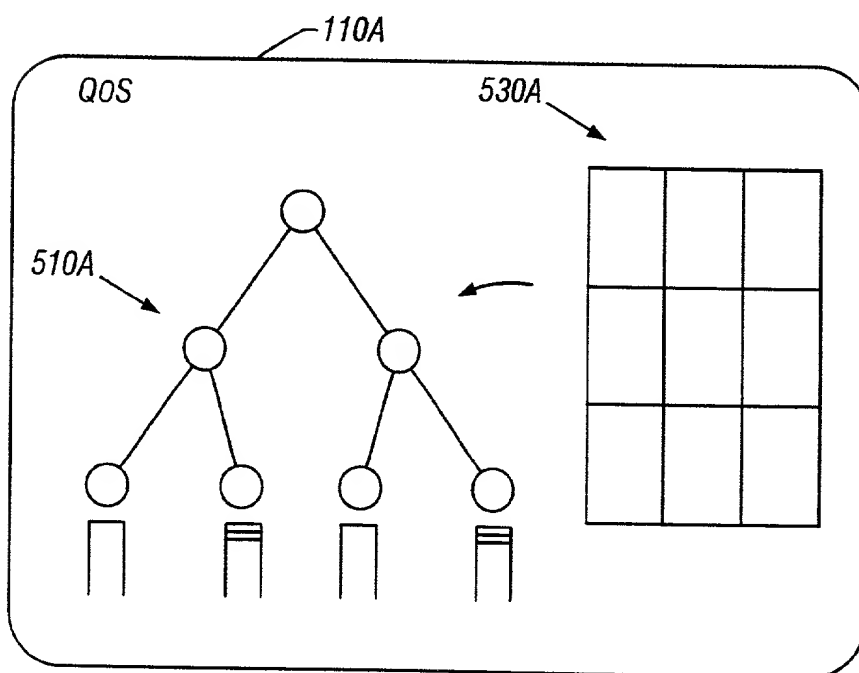
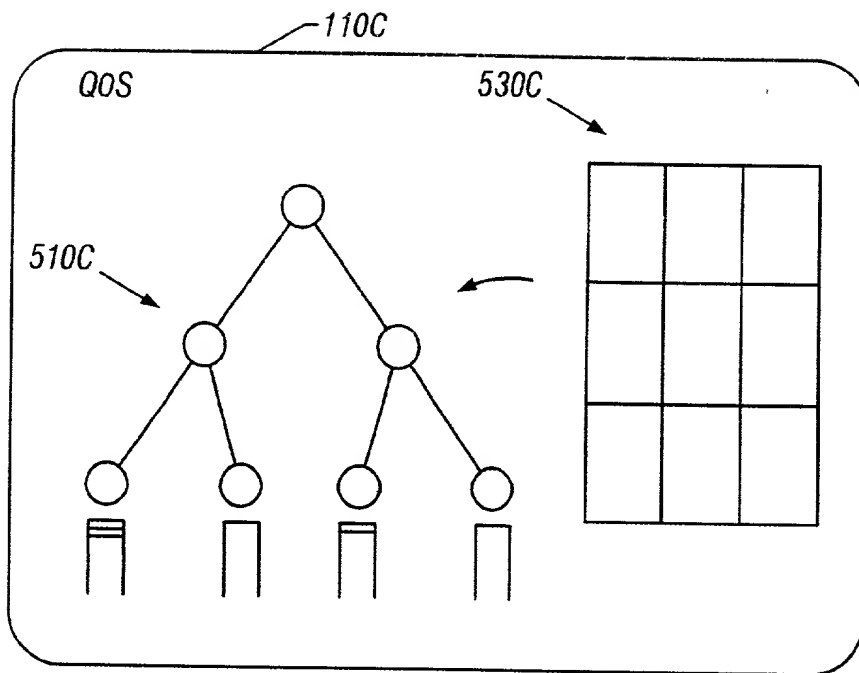


FIG. 5

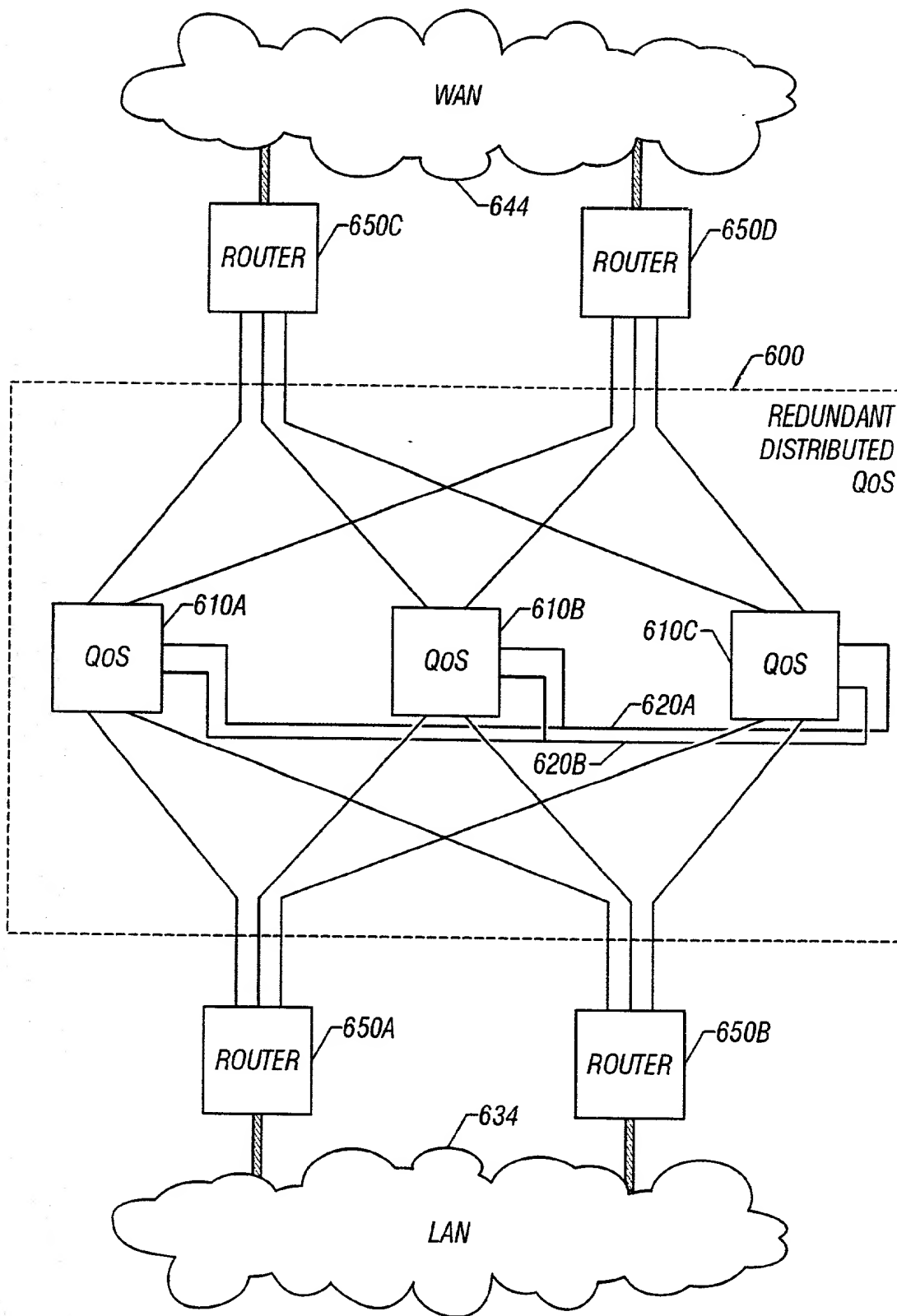


FIG. 6

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QoS PROCESSOR

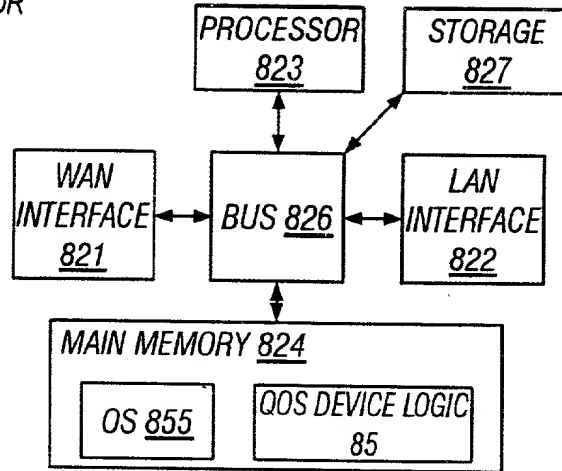


FIG. 7A

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QoS DEVICE LOGIC

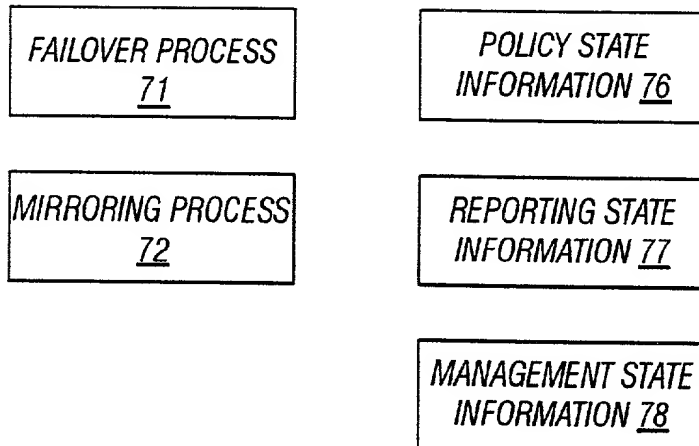


FIG. 7B

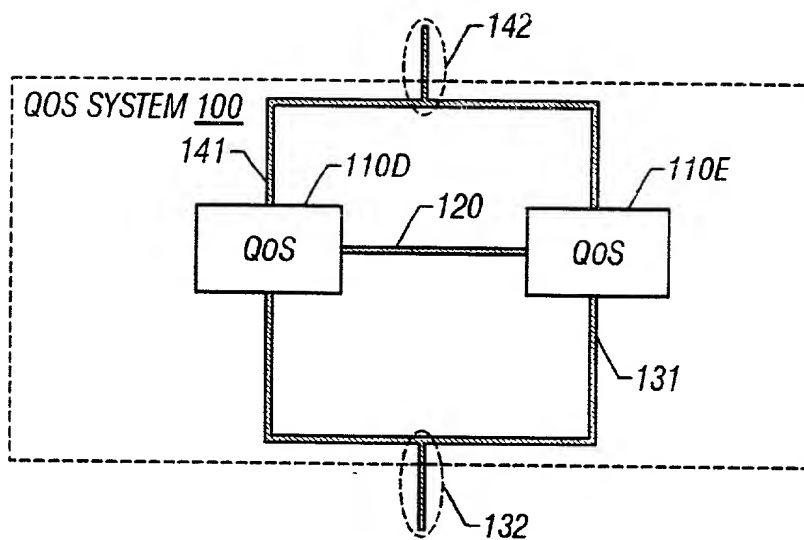


FIG. 8A

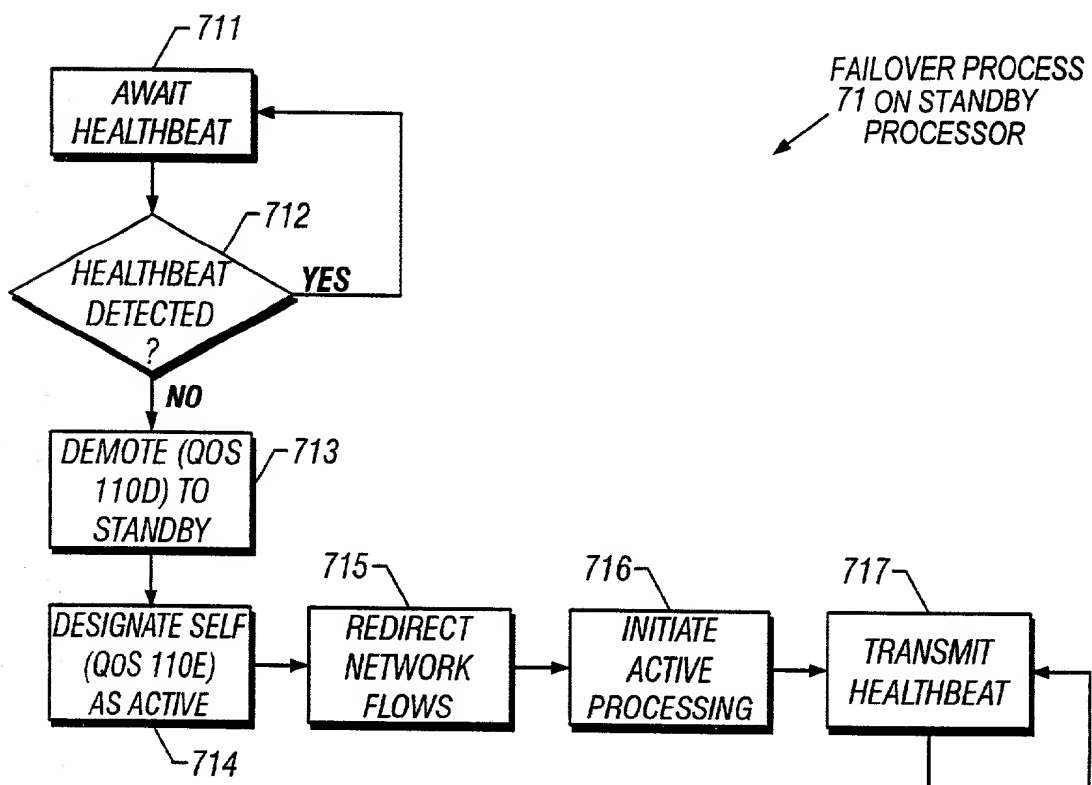


FIG. 8B

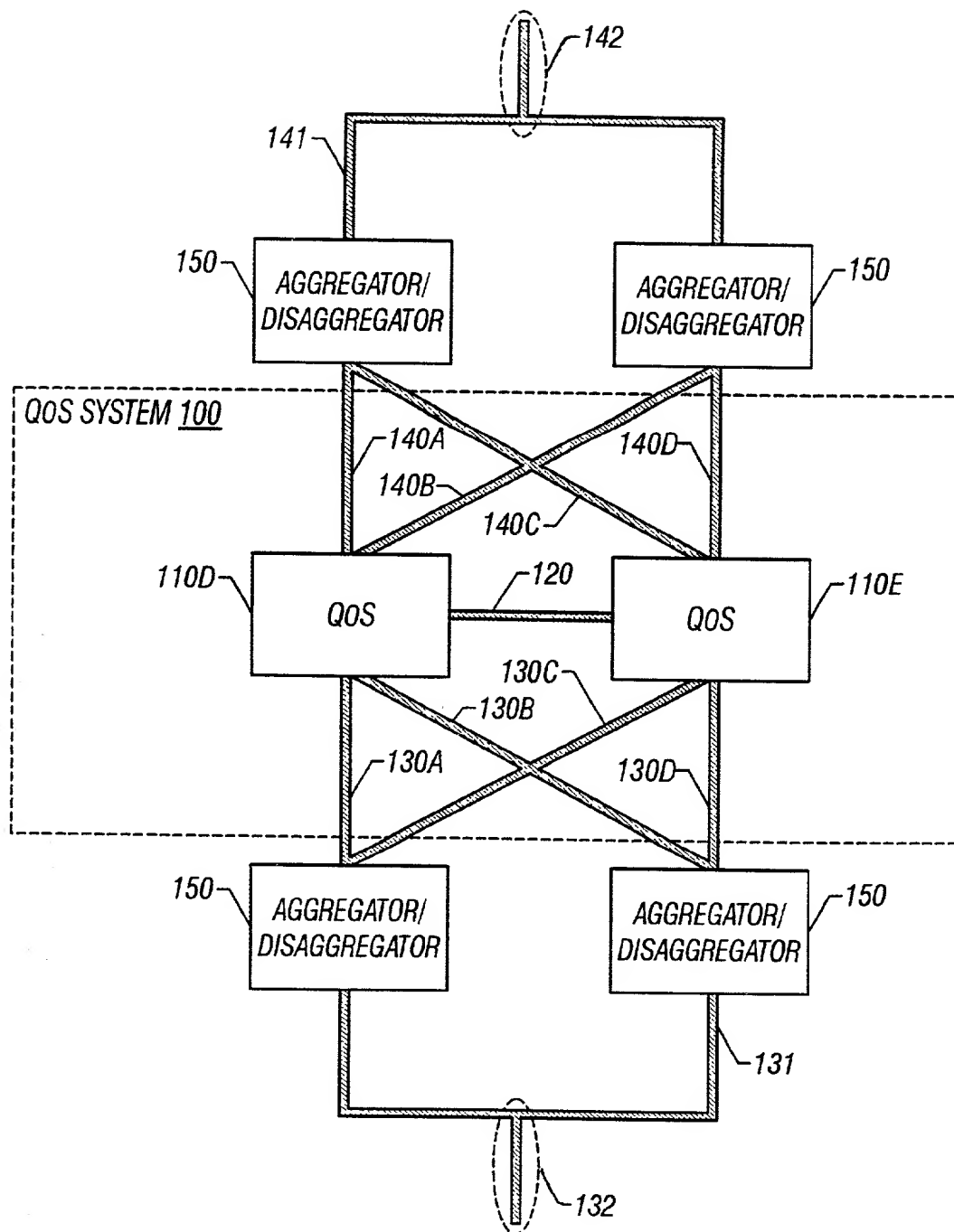


FIG. 9